**I. Modeling**

This document summarizes the modeling of following elements

1. Distribution Line
2. ZIP Load
3. Step Voltage Regulator
4. Switches

**1. Distribution Line Modeling**

The Distribution lines are modeled using

1. SimPower System Pi Section Line model
2. SimPower System Distributed Parameters Line model
3. **Simpower System Pi Section Line model**

This is an inbuilt model available in Simulink Library as shown in Fig 1. The line parameters R, L, and C are specified as positive and zero-sequence parameters that take into account the inductive and capacitive couplings between the three phase conductors, as well as the ground parameters. This method of specifying line parameters assumes that the three phases are balanced. This is a source of error since the lines are not balanced in the case of a distribution system.

Another model which is more accurate and that captures the unbalances in distribution line is the “Distributed Parameter Line” model. But the requirement of propagation time to be smaller than the specified Sample time value will make it difficult for dynamic simulation. In order to simulate this model, Sample time must be very much less than 0.005µs which is not feasible.

Diagram

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Fig 1: SimPower System Pi Section Model

The distribution line parameters in phase domain (as 3x3 matrices) provided in IEEE 13 bust benchmark document [1] is used to obtain the sequence parameters. The RXB matrices provided in Ω/mile are converted to Ω/km and length of line is converted from ft to km. Then the phase RLC 3x3 matrix values in Ohm, Henry and Farad respectively are calculated. The phase to sequence conversion is applied to obtain positive and zero sequence value of RLC. An example for line section 1 is shown below.

**Configuration 1:**

Z (R +jX) in ohms per mile

0.4576 1.0780 0.1560 0.5017 0.1535 0.3849

0.4666 1.0482 0.1580 0.4236

0.4615 1.0651

B in micro Siemens per mile

5.6765 -1.8319 -0.6982

5.9809 -1.1645

5.3971

a=1; % Loading Factor

Ts=50e-6;

% phase to sequence conversion of RXB matrices

c=1\*exp(sqrt(-1)\*(120\*pi/180));

T=[1 1 1;1 c\*c c;1 c c\*c];

Tinv=1/3\*[1 1 1;1 c c\*c;1 c\*c c];

% Simulation data for the IEEE 123 Node Test Feeder model

mi2km = 1.60934;% miles to km conversion

ft2km = 0.0003048; % feet to km conversion

ms2F = 1\*1e-6/(2\*pi\*60);% microsiemens to Farads conversion

%% Configuration 1

% Series Resitance and Reactance - ohm/mile

R\_1 = [0.4576 0.1560 0.1535;0.1560 0.4666 0.1580;0.1535 0.1580 0.4615];

X\_1 = [1.0780 0.5017 0.3849;0.5017 1.0482 0.4236;0.3849 0.4236 1.0651];

% charging susceptance - microsiemens/mile

B\_1 = [5.6765 -1.8319 -0.6982;-1.8319 5.9809 -1.1645;-0.6982 -1.1645 5.3971];

R\_1 = R\_1/mi2km;

Rseq=Tinv\*R\_1\*T;

R1\_1=Rseq(2,2);R0\_1=Rseq(1,1);

L\_1 = (X\_1/(2\*pi\*60))/mi2km;

Lseq=Tinv\*L\_1\*T;

L1\_1=Lseq(2,2);L0\_1=Lseq(1,1);

C\_1 =(B\_1\*ms2F)/mi2km;

Cseq=Tinv\*C\_1\*T;

C1\_1=Cseq(2,2);C0\_1=Cseq(1,1);

This code is provided in “InitFcn” section which can be located by right click🡪 Model Properties 🡪Callbacks in Simulink.

The sequence values of RLC and length are used as input in the Three Phase or Single-Phase Pi Section Line block as shown in Fig 2.

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Fig 2: SimPower System Three Phase Pi Section Input

1. **Simpower System Distributed Parameter Line model**

This is an inbuilt model available in Simulink Library as shown in Fig 2. The Distributed Parameter Line block implements an N-phase distributed parameter line model with lumped losses. The model is based on the Bergeron's traveling wave method. In comparison to the PI section line model, the distributed line represents wave propagation phenomena and line end reflections with much better accuracy.

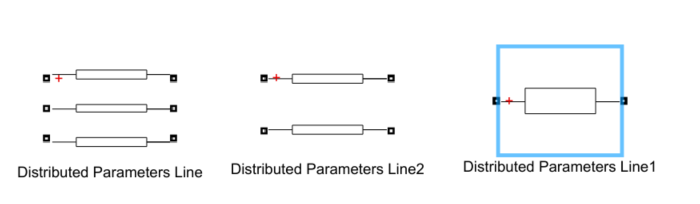


Fig 3: SimPower System Distributed Parameters Line

The phase RLC 3x3 matrix values in Ohms, Henry and Farad respectively is calculated and used as input to the custom-built Pi section model as shown in Fig 4.

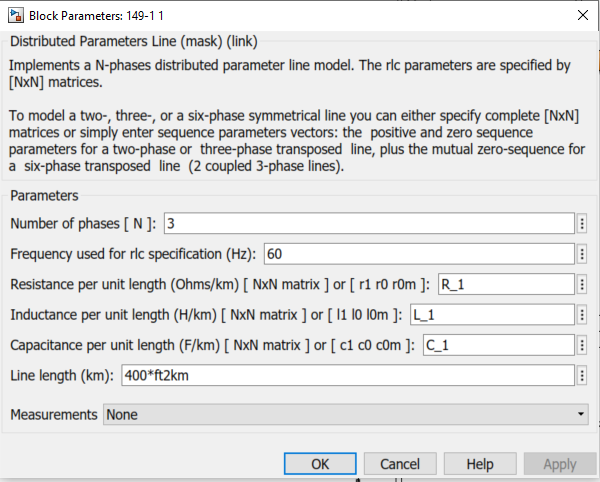


Fig 4: Custom Built Three Phase Pi Section Input

**2. ZIP Load Modeling**

The main drawback of using the Simpower System Three Phase and Single-Phase series RLC Loads is that different types of load models or ZIP models (Constant Impedance, Constant Current and Constant Power) are only activated while performing a load flow analysis using the Simulink Loadflow tool. While doing a dynamic simulation all these models will be considered as a constant impedance load which leads to differences in steady state voltages and powerflow compared to the benchmark document. Since simulink library does not have the ZIP load models functionality, custom models are developed.

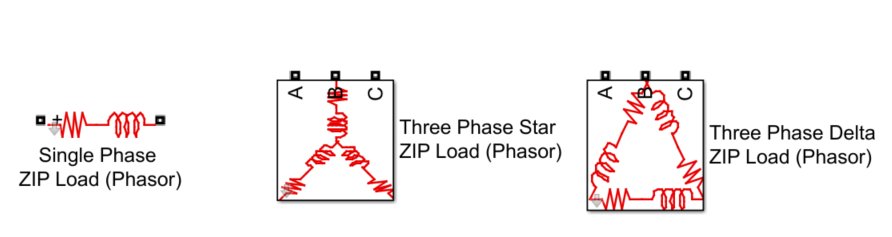
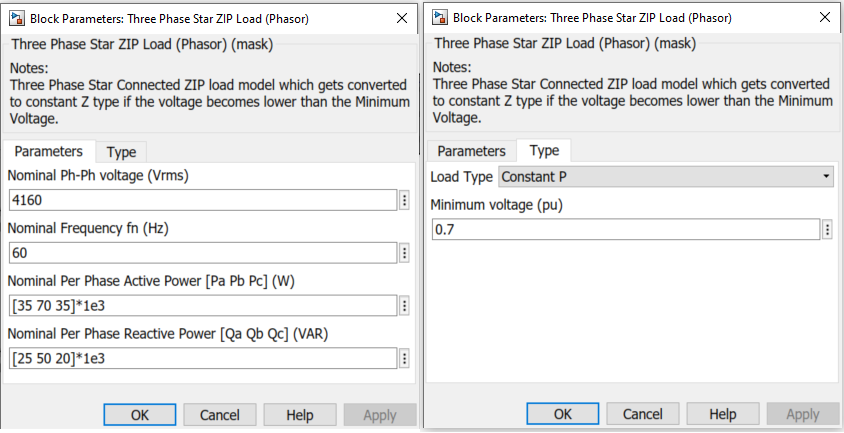
 

Fig 5: ZIP Load Modeling

Another key feature included in load modeling is the **loading factor (a)** as shown in Fig 5. This allows the user to run simulations with different loading. Note that this is a static functionality and cannot be changed during simulation to have a dynamic change in the loading. For such cases a dynamic load should be used. This loading factor can be varied by modifying value of **a** in the code provided in “InitFcn” section which can be located by right click🡪 Model Properties 🡪Callbacks in Simulink.

The Capacitor bank used for reactive power support is also modeled as a series load.

**3. Step Voltage Regulator**

Voltage regulators in the IEEE test distribution feeders are assumed to be “step-type” and are connected in the substation and also in specified line segments inside the feeder far from substation. The regulators can be three-phase or single phase. The changing of taps on a regulator is controlled by the Line Drop Compensator (LDC) when the voltage to be regulated is of a remote node far from the regulator. A simplified circuit of an analog compensator [2] and how it is connected to the feeder through a potential and current transformer is shown in Fig 6. In the transformer, there are four settings that are required for the compensator circuit.

They are

1. The compensator R and X setting
2. The reference voltage level setting
3. The bandwidth setting
4. The time delay setting.

The voltage setting gives the desired voltage to hold at the regulation point and bandwidth defines the allowed variance of the regulation point voltage centered at the desired Voltage Level. The time delay is the delay before a tap change is made when the voltage is not within the bandwidth.

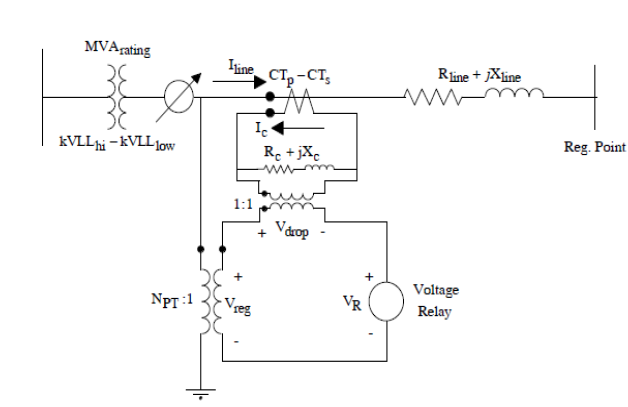


Fig 6: Analog Compensator

The goal of the compensator circuit is such that the voltage across the compensator voltage relay will be a scale model of the actual voltage at the regulation point. The per-unit voltage of the compensator voltage relay should be equal to the per-unit voltage at the regulation point. In order to make this happen the per-unit R and X settings must be equal to the per-unit equivalent line impedance from the regulator output to the regulation point.

A single phase SVR is modeled and combination of these are used to create three phase SVRs as shown in Fig 7. SVR consists of a variable ratio transformer, LDC and voltage regulator control. The variable-ratio transformer has taps on the secondary of the transformer. The turns ratio is calculated by the LDC and voltage regulator control. The turns ratio is calculated as

**N=**

Therefore, to raise the secondary voltage, a tap up operation is required and vice versa to lower the voltage. A maximum/minimum tap is also included so that the regulator would not tap beyond a certain point. The input parameters required for SVR are shown in Fig 8.

In case of IEEE 123 bus system, the SVR at substation is a ganged type. The taps of all phases are operated together based on voltages of phase A as shown in Fig. 9.

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Diagram

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Fig 7: Step Voltage Regulator Model

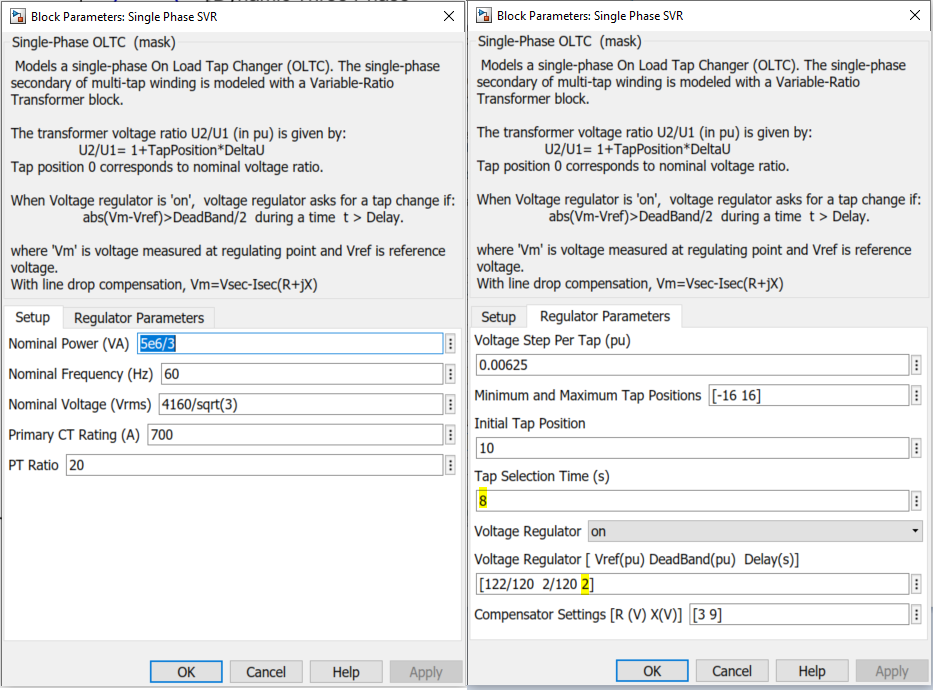


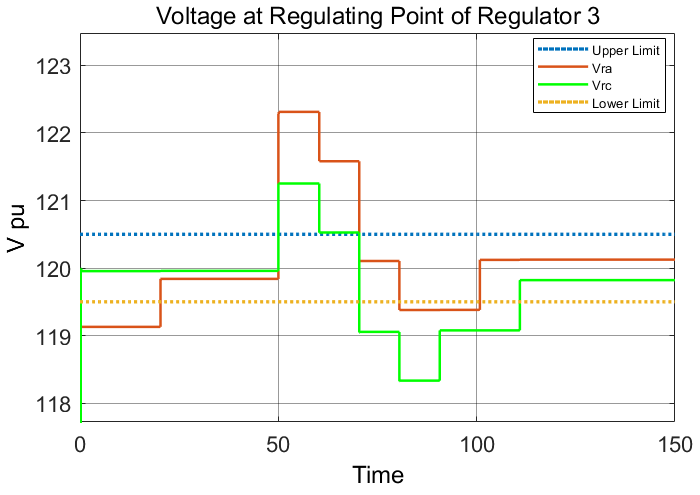
Fig 8: Step Voltage Regulator Model Inputs

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Fig 9: Ganged Step Voltage Regulator Model

A test is conducted where breaker between 13-152 is opened at 50s. The reference voltage of regulator 3 is chosen as 122V, bandwidth as 1V and delay as 20s (Tap selection time+Delay as in Fig 8). The reference voltage of regulator 1 is chosen as 120V, bandwidth as 2V and delay as 10s. It can be seen that as soon as load is reduced due to switching at 50s, the voltage rises at regulating point of regulator 3 (as well as at regulating point of regulator 1) and goes out of bandwidth. Since regulator 1 has smaller delay, it starts tapping down first after 10 sec. The voltage is still out of bound, regulator 3 taps after its delay time of 20 sec. Therefore at 70 sec, the drop in voltage is due to tap reduction at both regulator 1 and regulator 3. The voltage is now within limit. But regulator 1 taps again which will result in voltage drop at primary and secondary of regulator 3. Now voltage at both phases is lower than the lower limit. This results in tap up operation of regulator 3 until voltages are within the bandwidth. It is only possible to run simulations of this timescale if phasor domain is used. The simulation of 150sec took around 5 sec to complete.



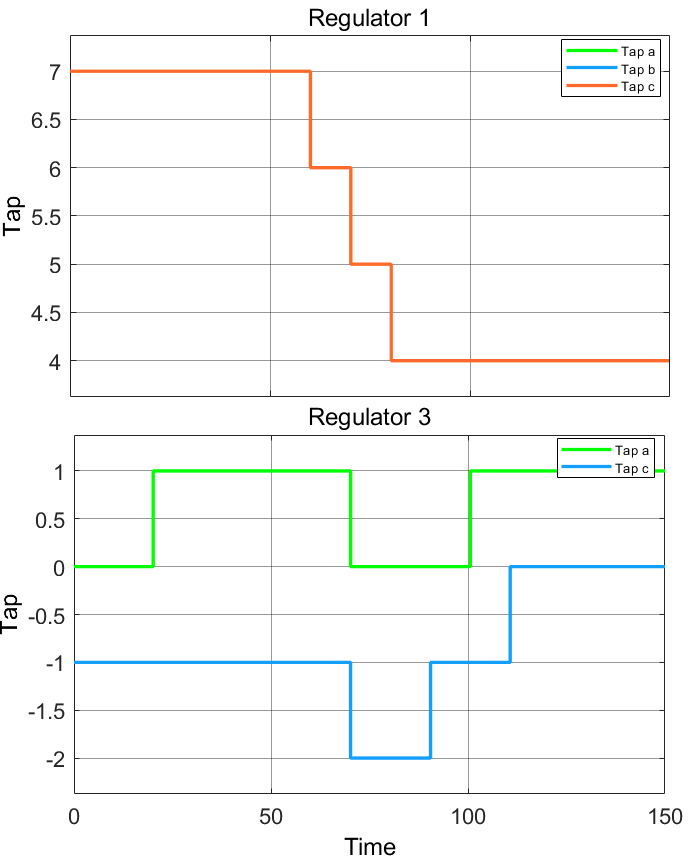


Fig 10: Tap Operation

**4. Switches**

The switches are modeled using the Three Phase Breaker model as shown in Fig 11. The status of breakers is taken similar to the benchmark document. A combination of these breakers can be controlled by external signal and can be used to connect/disconnect parts of feeder and hence making multiple islands which can be used to conduct grid forming inverter studies.

Chart, diagram, box and whisker chart

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Fig 11: Measurement Subsystems

**II. Measurement Subsystems**

In order to capture all dynamics of the model, Three-Phase VI Measurement blocks are connected to every node. The voltage, current and calculated power measurements can be used as inputs to optimization routines, adaptive or optimal controllers as discussed in [3] and [4]. Three measurement subsystems are created namely Node Voltages subsystem, Regulator Taps subsystem and Substation Power subsystems as shown in Fig 12. The Node Voltage subsystem collects all node voltages in pu and store it in a variable V13 with 0.1sampling time. The Regulator Tap subsystem shows the voltage profile at regulating point with reference voltages and bandwidths. The Substation Power subsystem calculates the head end (substation) power.

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Fig 12: Measurement Subsystems

**III. Assumptions and Approximations**

Two main assumptions and approximations are

* Dynamic Simulation in Simulink will consider all Loads as Constant Impedance Loads.

These assumptions will result in small differences in voltage profile compared to the voltage profile given in the Benchmark Document.

**IV. References**

[1] <https://cmte.ieee.org/pes-testfeeders/resources/>

[2] W. Kersting, Distribution System Modeling and Analysis, ser. The Electric Power System Engineering Series. CRC Press, 2002.

[3] A. Suresh, R. Bisht and S. Kamalasadan, "A Coordinated Control Architecture With Inverter-Based Resources and Legacy Controllers of Power Distribution System for Voltage Profile Balance," in IEEE Transactions on Industry Applications, vol. 58, no. 5, pp. 6701-6712, Sept.-Oct. 2022, doi: 10.1109/TIA.2022.3183030.

[4] A. Suresh, R. Bisht and S. Kamalasadan, "ADMM Based LQR for Voltage Regulation Using Distributed Energy Resources,"2020 IEEE International Conference on Power Electronics, Drives and Energy Systems (PEDES), 2020, pp. 1-6, doi: 10.1109/PEDES49360.2020.9379625.